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SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(PTO-1449)

U.S. PATENT DOCUMENTS

APPLICANT

FILING DATE
April 28, 2005

SERIAL NO.
10/532.993

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 061352-0100	SERIAL NO. 10/532,993		
				APPLICANT Michihito UEDA			
				FILING DATE April 28, 2005	GROUP Not yet assigned		
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Code-Number-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes	No
/JG/		JP 48-102557 A	12/22/1973			partial only	
OTHER ART (Including Author, Title, Date, Berlitzent Pages, Etc.)							
EXAMINER'S INITIALS	CITE NO.	Include name or the author (in CAPITAL LETTERS), title of this article (which is appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
		YAMANAKA et al.; "A CMOS Stochastic Associative Processor Using PWM Chaotic Signals"; <i>IEICE Trans. Electron.</i> ; c. 2001; Vol. E84, No. 12; pp. 1723-1729					
EXAMINER <i>/James Goodley/</i>				DATE CONSIDERED 03/20/2008			

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/JG/		YAMANAKA et al.; "A CMOS Stochastic Associative Processor Using PWM Chaotic Signals"; <i>IEICE Trans. Electron.</i> ; c. 2001; Vol. E84, No. 12; pp. 1723 - 1729	

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